Vivekananda College of Engineering & Technology,Puttur [A Unit of Vivekananda Vidyavardhaka Sangha Puttur ®]							
Affiliated to VTU, Belagavi & Approved by AICTE New Delhi							
CRM08	Rev 1.10	<cse></cse>	<16.06.2021>				

## CONTINUOUS INTERNAL EVALUATION- 2

Dept: CSE	Sem / Div: 4CS A & B	Sub: Microcontroller &	S Code: 18CS44					
		Embedded Systems						
Date: 25.06.2021	Time: 3:00-4:30	Max Marks: 50	Elective: N					
Note: Answer any 2 full questions, choosing one full question from each part.								

	Q	Questions	Marks	RBT	COs			
F	PART A							
1	a	Write a function in assembly that can sum any number of integers. The	9	L3	CO2			
		arguments should be the number of integers to sum followed by a list						
		of the integers						
	b	What is an Embedded System? Explain the different classifications of	9	L2	CO3			
		embedded systems. Give example for each						
	c	Differentiate –	7	L3	CO3			
		(i) Microorcessors versus Microcontrollers						
		(ii) CISC versus RISC Processors						
		OR						
2	a	Justify the statement with Examples: By combining conditional	9	L3	CO2			
		execution and conditional setting of the flags, you can implement						
		simple if statements without any need for branches.						
	b	What is Embedded System? Illustrate any four purpose of embedded	9	L3	CO3			
		systems						
	c	Differentiate –	7	L3	CO3			
		(i) CPLD versus FPGA						
		(ii) SRAM versus DRAM						
PART B								
3	a	What is Programmable Logic Device (PLD)? What are the different	9	L2	CO3			
		types of PLDs? Explain advantages of PLDs in embedded system						
		design						
	b	What is Stepper Motor? Explain different step modes. Also, explain	9	L2	CO3			
		the role of stepper motor in embedded applications						
	c	Explain the concept of memory shadowing. Give its advantages	7	L3	CO3			
OR								
4	a	What are different types of memories used in embedded system design	9	L2	CO3			
	b	Explain the different on-board communication interfaces in brief	9	L2	CO3			
	c	What is Programmable Peripheral Interface (PPI)? Explain the	7	L3	CO3			
L		interfacing of 8255 PPI with an 8-bit processor/ controller						

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